



# City Research Online

## City, University of London Institutional Repository

---

**Citation:** Ul Asad, H. & Jones, K. (2015). Verifying inevitability of phase-locking in a charge pump phase lock loop using sum of squares programming. In: Proceedings of the 25th edition on Great Lakes Symposium on VLSI. (pp. 295-300). New York, USA: ACM. ISBN 9781450334747 doi: 10.1145/2742060.2742072

This is the accepted version of the paper.

This version of the publication may differ from the final published version.

---

**Permanent repository link:** <https://openaccess.city.ac.uk/id/eprint/12325/>

**Link to published version:** <https://doi.org/10.1145/2742060.2742072>

**Copyright:** City Research Online aims to make research outputs of City, University of London available to a wider audience. Copyright and Moral Rights remain with the author(s) and/or copyright holders. URLs from City Research Online may be freely distributed and linked to.

**Reuse:** Copies of full items can be used for personal research or study, educational, or not-for-profit purposes without prior permission or charge. Provided that the authors, title and full bibliographic details are credited, a hyperlink and/or URL is given for the original metadata page and the content is not changed in any way.



# Verifying Inevitability of Phase-locking in a Charge Pump Phase Lock Loop using Sum of Squares Programming

Hafiz ul Asad

School of Mathematics, Computer Science and Engineering  
City University London  
EC1 0HB London, UK  
hafiz.ul-asad.1@city.ac.uk

Kevin D. Jones

Plymouth University  
PL4 8AA Plymouth, UK  
kevin.jones@plymouth.ac.uk

## ABSTRACT

Phase-locking in a charge pump (CP) phase lock loop (PLL) is said to be inevitable if all possible states of the CP PLL eventually converge to the equilibrium where the input and output phases are in lock. We verify this property for a CP PLL using a mixed deductive and bounded verification methodology. This involves a positivity check of polynomial inequalities (which is an NP-Hard problem) so we use the sound but incomplete Sum of Squares (SOS) relaxation algorithm to provide a numerical solution.

## 1. INTRODUCTION

Formal methods are in their infancy in Analog and Mixed Signal (AMS) circuits verification. Start up problems have been very common in PLL circuits, i.e., for certain initial states of voltages, the circuits do not converge to the desired behaviour. In addition, while in phase-locking state and disturbed by an external input, it is important to know whether the PLL circuit retains its locking state. Verifying both these properties are closely related to the verification of inevitability property.

Hybrid systems are well known modelling paradigm for a CP PLL [16], [2], [6]. Techniques for the verification of hybrid systems can be classified as, reach-set methods, abstraction based methods, and certificate based methods. Reachability has been used to prove the above stated property. Hundreds of discrete transitions are required by the hybrid model of the CP PLL before it reaches the locking state. This results in large number of continuous set computations followed by the guard conditions describing the switching laws, making the reachability computation prohibitively expensive. In this paper, we use a mixture of certificate based deductive and bounded (reach-set) verification methodology. We verify the inevitability of the phase-locking in a CP PLL by adopting a two-pronged verification approach. Due to complexity of the property, we essentially divide the inevitability property in to the conjunction of two sub-properties. These

Permission to make digital or hard copies of all or part of this work for personal or classroom use is granted without fee provided that copies are not made or distributed for profit or commercial advantage and that copies bear this notice and the full citation on the first page. Copyrights for components of this work owned by others than ACM must be honored. Abstracting with credit is permitted. To copy otherwise, or republish, to post on servers or to redistribute to lists, requires prior specific permission and/or a fee. Request permissions from permissions@acm.org

DAC'15, June 07 - 11, 2015, Pittsburgh, PA, USA. Copyright is held by the owner/author(s). Publication rights licensed to ACM.

ACM 978-1-4503-3520-1/15/06 ...\$15.00.

<http://dx.doi.org/10.1145/2744769.2744926>.

two properties determine the truth value of the inevitability property in two disjoint subsets of the state space. The first property specifies, that in a compact set, all system trajectories eventually converge to the equilibrium locking state. The second property is specified, such that the set where the first property holds, is reachable from the second subset of the state space. The first property is verified by computing an attractive region (AR) utilizing the deductive Lyapunov stability theory for hybrid systems [5]. We construct multiple Lyapunov certificates for different modes of the CP PLL hybrid system. The maximized level curves of these Lyapunov certificates characterize the level sets whose union is the AR. We verify the second property utilizing bounded advection of level sets ([15]), and deductive Escape certificate method, showing the reachability of this AR from the states outside it. The deductive and bounded verification approaches involve checking positivity of polynomial inequalities, which is an NP-hard problem. We use the sound but incomplete SOS relaxation for the positivity verification of polynomial inequalities.

## 1.1 Related Work

A survey of the formal verification of AMS circuits can be found in [17]. In [6], the author verified the ‘time to locking’ property for a digitally extensive PLL. [16] verified ‘global convergence’ property for an all digital PLL. They divided the state space into linear and non-linear regions, and applied linear Lyapunov stability theory (Using Quadratic Lyapunov Certificate) for linear and reachability analysis for non-linear regions respectively. Time-outs of the reachability tool has been reported by the author due to the large number of discrete transitions needed by the PLL hybrid automata. To avoid discrete jumps, [2] presented a continuization technique and verified the ‘time to locking’ property of a CP PLL.

Hybrid models have different flavours that can be found in [8],[1]. Here we consider the framework outlined in [4]. In the last decade, SOS programming has been the major tool used in the algorithmic construction of Lyapunov certificates for continuous, as well as hybrid systems [9], [11]. Deductive verification of continuous and hybrid systems have been demonstrated in [14], [13]. Recently an advection algorithm for polynomial level sets based on SOS programming has been presented in [15]. This advection algorithm has been used for reachable sets estimation for continuous dynamical systems. We extend this approach to bounded verification of the hybrid systems.

This paper is organized as follows: In Sec.II, we introduce the preliminaries of this paper. Sec.III illustrates verification of the inevitability of phase-locking in CP PLL. Experimental results are shown in Sec.IV. Sec.V concludes the paper.

## 2. PRELIMINARIES

### 2.1 Hybrid Systems Model

We use the hybrid system formalism described in [4]. We consider a hybrid system described by the tuple  $(\mathcal{C}, \mathcal{F}, \mathcal{D}, \mathcal{G})$ . Here,  $\{\mathcal{C} = \bigcup_{i \in I_C} C_i\} \subset \mathbb{R}^n$ , and  $\{\mathcal{D} = \bigcup_{i \in I_D} D_i\} \subset \mathbb{R}^n$  are the flow set and jump set for  $i \in \mathbb{N}$ , respectively.  $I_C$  and  $I_D$  are finite disjoint index sets and it is possible that  $C_i \cap D_i \neq \emptyset$ . The flow and jump maps are, respectively,  $\mathcal{F} = \bigcup_{i \in I_C} F_i$ , and  $\mathcal{G} = \bigcup_{i \in I_C} G_i$ , where each  $F_i : \mathbb{R}^n \times \mathbb{R}^m \rightarrow \mathbb{R}^{m+n}$ , and,  $G_i : \mathbb{R}^n \rightarrow \mathbb{R}^n$ . These two mappings characterize the continuous and discrete evolution of the system, whereas  $C_i$  and  $D_i$  describe subsets of  $\mathbb{R}^n$  where such evolution may occur. We represent a hybrid system  $\mathcal{H}$  as

$$\mathcal{H} = \begin{cases} \dot{x} = F_i(x, u) \in \mathcal{F} & x \in \mathcal{C}, u \in \mathcal{U} \\ x^+ = G_i(x) \in \mathcal{G} & x \in \mathcal{D} \end{cases} \quad (1)$$

Here  $u \in \mathcal{U} \subset \mathbb{R}^m$  is a vector of uncertain parameters. The state of the hybrid system consists of alternate flows in jumps through  $\mathcal{C}$  and  $\mathcal{D}$  according to  $F_i$  and  $G_i$ , respectively. This hybrid phenomena can be described by a notion of time called hybrid time.

**Definition 1** (Hybrid Time Domain).

A set  $\mathcal{T} \subset \mathbb{R}_{\geq 0} \times \mathbb{N}$  is a hybrid time domain if

$$\mathcal{T} = \bigcup_{j=0}^{j-1} ([t_j, t_{j+1}], j)$$

where  $0 = t_0 \leq t_1 \leq t_2 \leq \dots$ , with the last interval possibly of the form  $[t_j, t_{j+1}] \times \{j\}$ ,  $[t_j, t_{j+1}] \times \{j\}$ , or  $[t_j, \infty) \times \{j\}$ .

**Definition 2** (Hybrid Arc).

A mapping  $x : \mathcal{T} \rightarrow \mathbb{R}^n$  is a hybrid arc if  $\mathcal{T}$  is a hybrid time domain and for each  $j \in \mathbb{N}$ , the function  $t \mapsto x(t, j)$  is locally absolutely continuous on the interval  $I_j = \{t : (t, j) \in \mathcal{T}\}$ .

**Assumption 1.**

The flow maps  $F_i(x, u)$  and the jump maps  $G_i(x)$  are polynomials.

**Definition 3** (Equilibrium point).

A point  $x(t, j) \in \mathcal{C} \cup \mathcal{D}$  is called an equilibrium, if  $\exists t, \exists j, F_j(x(t, j), u) = 0$ .

**Definition 4** (Inevitability of Equilibrium).

The equilibrium point  $x_e$  is said to be inevitable, if  $\forall x(0, j) \in \mathcal{C} \cup \mathcal{D}$  and bounded  $t$ ,  $x(t, j) \rightarrow x_e$ .

### 2.2 CP PLL Model

A CP PLL circuit consists of a reference signal, a phase frequency detector (PFD), a charge pump (CP), a loop filter (LF), and a voltage controlled oscillator (VCO). In this paper we consider a single path higher order (Third and fourth) CP PLL shown in Fig. 1<sup>1</sup>. We use a behavioural model of the PLL, where we consider a linear model for VCO, a linear model for the third order LF, and a non-linear model for

<sup>1</sup>Third order shown here,fourth order has an additional resistor-capacitor in its LF

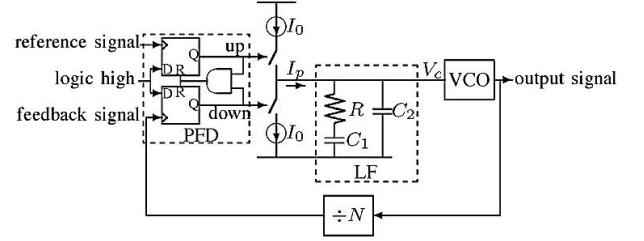


Figure 1: Charge Pump (CP) Phase Lock Loop (PLL)

the PFD. We denote by  $\phi_{ref}$ , and  $\phi_{VCO}$ , the phases of the reference and VCO output feedback signals respectively. We model the CP PLL as a hybrid system such that the nonlinearities of the PFD is modelled as a piecewise continuous signal. Ignoring the cycle slip phenomena, the PFD output in the form of the charge pump current  $I_p$ , is given by the following piecewise linear inclusion:

$$I_p = \begin{cases} \in [I_p^U, I_p^D] & \text{UP}=1, \text{Down}=0, 0 \leq \phi_{VCO} < 2\pi \leq \phi_{ref} \\ \in [I_p^D, I_p^U] & \text{UP}=0, \text{Down}=1, 0 \leq \phi_{ref} < 2\pi \leq \phi_{VCO} \\ \in [0^R, 0^R] & \text{UP}=0, \text{Down}=0, 0 \leq \phi_{VCO}, \phi_{ref} < 2\pi \end{cases} \quad (2)$$

We denote the three modes as mode1 ( $\text{UP}=0, \text{Down}=0$ ), mode2 ( $\text{UP}=1, \text{Down}=0$ ) and mode3 ( $\text{UP}=0, \text{Down}=1$ ). The transition from one mode to another is based on the reference and feedback signals hitting the  $2\pi$  threshold. Due to the cyclic behaviour of the PLL and to keep the analysis modulo  $2\pi$ , we need to ensure the phases remain in the range  $0 \leq \phi_{VCO}, \phi_{ref} < 2\pi$  (Similarly  $\phi_{ref}$ ) after resetting the PFD. This is achieved by resetting the two phases such that  $(\phi_{ref} := 0, \phi_{VCO} := \phi_{VCO} - 2\pi)$ , and  $(\phi_{ref} := \phi_{ref} - 2\pi, \phi_{VCO} := 0)$ , while taking transitions from mode1 to mode2 and mode1 to mode3, respectively. Identity resets are used for transitions from mode2 to mode1 and mode3 to mode1.

Our model consists of the state variables,  $\phi_{VCO}$ ,  $\phi_{ref}$ , voltage  $v_1$  across the capacitor  $C_1$ , and the voltage  $v_2$  across the capacitor  $C_2$  (Fourth order has an additional voltage variable across the third capacitor). Let  $f_{VCO}$ , and  $f_{ref}$ , represent the frequencies of the VCO output and the reference signal respectively. If  $K_p$  is the gain of the LF, then  $f_{VCO} = K_p v_2 / 2\pi + f_o$ , where  $f_o$  is the free running frequency of the VCO. Therefore,  $\dot{\phi}_{VCO} = 2\pi f_{VCO}/N$ ,  $\dot{\phi}_{ref} = 2\pi f_{ref}$ . By Kirchhoff's current law and using the three modes of the PFD, we get the following hybrid system of the third order CP PLL

$$\mathcal{H} = \begin{cases} \dot{v}_1 \\ \dot{v}_2 \\ \dot{\phi}_{ref} \\ \dot{\phi}_{VCO} \\ x^+ = G_i(x) \end{cases} = A \begin{pmatrix} v_1 \\ v_2 \\ \phi_{ref} \\ \phi_{VCO} \end{pmatrix} + BI_p + c \quad x \in \mathcal{C}, \\ x \in \mathcal{D} \quad (3)$$

$$\text{where, } A = \begin{pmatrix} -1/RC_1 & 1/RC_1 & 0 & 0 \\ 1/RC_2 & -1/RC_2 & 0 & 0 \\ 0 & 0 & 0 & 0 \\ 0 & K_p/N & 0 & 0 \end{pmatrix},$$

$$B = \begin{pmatrix} 0 \\ I_p/C_2 \\ 0 \\ 0 \end{pmatrix}, c = \begin{pmatrix} 0 \\ 0 \\ 2\pi f_{ref} \\ 2\pi f_o/N \end{pmatrix}, I_p \text{ as given by Eq. 2.}$$

From the three modes and their invariants, we can easily find the sets  $\mathcal{C}$ ,  $\mathcal{D}$ , and the jump maps  $G_i(x)$ . Here, we use the difference  $\phi_{ref} - \phi_{VCO}$  as a state variable instead of  $\phi_{ref}$ , and  $\phi_{VCO}$ .

#### Remark 1.

This change of state variables transforms all jump maps  $G_i$  in to identity maps ( $G_i(x) = x$ ), as the same constant  $2\pi$  is subtracted from  $\phi_{VCO}$  and  $\phi_{ref}$ , leaving their difference  $\phi_{ref} - \phi_{VCO}$  before and after the jumps unchanged.

### 2.3 Attractive Invariants in Hybrid Systems using Lyapunov Certificates

Contrary to safety properties where existence of an invariant set is sufficient for proving/dis-proving the property, for inevitability property, we use the concept of "attractive invariants". Attractive invariants are compact semi-algebraic sets where if the CP PLL hybrid system starts, will always remain there and will eventually converge to the equilibrium state. Stability and attractivity concepts for an equilibrium state of the continuous dynamical systems are discussed in [5], and have been extended to hybrid systems in [4]. The equilibrium point,  $x_e = 0$ , is called asymptotically stable (AS) if it is both stable and attractive. There are several versions of the stability theorems for hybrid systems based on the global Lyapunov function, and multiple Lyapunov certificates [12]. We use the following theorem of AS and define the attractive invariant,

#### Theorem 1.

Let,  $\mathcal{I}_0 \subseteq I_C$  be the set of indices that contain the equilibrium. For a hybrid system  $\mathcal{H}$  having an equilibrium point  $x_e = 0$ , if there exist Lyapunov certificates  $V_i$  such that,

1.  $V_i(x) > 0, \forall i \in I_C, \forall x \in \mathcal{C} \setminus x_e ,$
2.  $V_i(0) = 0, \forall i \in \mathcal{I}_0,$
3.  $\frac{\partial V_i}{\partial x}(x) F_i(x, u) < 0, \forall i \in I_C, \forall x \in \mathcal{C} \setminus x_e, F_i \in \mathcal{F}, u \in \mathcal{U},$
4.  $V_j(G_i(x)) - V_{j'}(x) < 0, \forall j \forall j' \in I_C, j \neq j', \forall i \in I_D, \forall x \in \mathcal{D} \setminus x_e, G_i \in \mathcal{G},$

then  $x_e$  is asymptotically stable. Furthermore, the set  $\mathcal{A}\mathcal{I} = \bigcup_i (V_i \leq c_{max}) \subset \mathcal{C} \cup \mathcal{D}$  is an "attractive invariant" set.

*Proof.* Similarly to [12].  $\square$

We also use another important characteristic of the trajectories in a semi algebraic set and term it as the Escape property. This property ensures that trajectories in a compact set can not converge to an invariant set (Equilibrium,Limit Cycle) and will eventually leave that set.

#### Proposition 1.

For a compact set  $\mathcal{X} \subset C_i$ , if there is a differentiable Escape certificate,  $E : \mathbb{R}^n \rightarrow \mathbb{R}$ , and  $\epsilon > 0$ , such that

$$\frac{\partial E}{\partial x}(x) F_i(x, u) \leq -\epsilon, \forall x \in \mathcal{X}, u \in \mathcal{U} \quad (4)$$

then  $\forall x(t, i) \in \mathcal{X}, x(t+T, i) \notin \mathcal{X}, T > t$ .

*Proof.* Assume that there exists  $x_0 \in \mathcal{X}$  such that  $x(t, i)$  starting at  $x_0$  remain in  $\mathcal{X}$  as  $t \rightarrow \infty$ . From equation. 4,  $E(x) = \int_0^\infty \frac{\partial E}{\partial x}(x) F_i(x) \leq -\epsilon$ . As  $t \rightarrow \infty$ ,  $E(x) \rightarrow -\infty$ . This contradicts the assumption as  $E(x)$  should be bounded if  $x(t, i)$  has to be in the bounded set  $\mathcal{X}$ . Therefore,  $x(t, i)$  has to eventually escape  $\mathcal{X}$  in finite time.  $\square$

### 2.4 SOS Programming

Our hybrid deductive and bounded verification approach involves checking the positivity of polynomials in semi-algebraic sets. To solve this NP-Hard problem, a sound relaxation method based on SOS programming has been discussed in [10], [12]. A sufficient condition for a multivariate polynomial  $p(x)$  to be non-negative everywhere, is that it can be decomposed as a sum of squares of polynomials. A polynomial  $p(x)$  is a sum of squares, if there exist polynomials  $p_1(x), \dots, p_m(x)$  such that  $p(x) = \sum_{i=1}^m p_i^2(x)$ . We denote the set of polynomials in  $n$  variables with real coefficients by  $\mathcal{P}_n$ . A subset of this set is the set of SOS polynomials in  $n$  variables denoted by  $\mathcal{S}_n$ . For a polynomial  $q : \mathbb{R}^n \rightarrow \mathbb{R}$ , differentiable scalar function, we define the 0-sub-level-set of  $q$  as  $\mathcal{Z}(q) = \{x \in \mathbb{R}^n \mid q(x) \leq 0\}$ . We present an important lemma to be used for polynomial level sets operations such as intersection, union, and set inclusion [15].

#### Lemma 1.

For polynomials  $p_1, p_2 \in \mathcal{P}_n$ , if there exist SOS polynomials  $s_0, s_1 \in \mathcal{S}_n$  such that

$$s_0 - s_1 p_1 + p_2 = 0 \quad \forall x \in \mathbb{R}^n \quad (5)$$

Then  $\mathcal{Z}(p_1) \subset \mathcal{Z}(p_2)$

*Proof.* See for example [15] and the references there in.  $\square$

### 2.5 Bounded Advection of Level Sets

Let we define a flow map  $\psi : \mathbb{R}^n \times \mathbb{R}_{\geq 0} \rightarrow \mathbb{R}^n$  for the hybrid system  $\mathcal{H}$ . A time  $t$  advection operator  $A_t$  is a map  $A_t : Q(\mathbb{R}^n, \mathbb{R}) \rightarrow Q(\mathbb{R}^n, \mathbb{R})$ , such that  $U = A_t V$  for  $U, V \in Q(\mathbb{R}^n, \mathbb{R})$ , and  $U(x) = V(\psi_{-t}(x))$  for all  $x \in \mathcal{C} \cup \mathcal{D}$ . This advection operator has an important property of linearity. For polynomial functions  $U_1, U_2 \in Q(\mathbb{R}^n, \mathbb{R})$ , if  $U_2 = A_t U_1$ , then  $\mathcal{Z}(U_2) = A_t(\mathcal{Z}(U_1))$ . For a detailed discussion on the advection operator see [15].

## 3. VERIFICATION METHODOLOGY

To verify inevitability of the CP PLL equilibrium, we introduce two compact sets  $\mathcal{S}_1$ , and  $\mathcal{S}_2$ , such that  $\mathcal{S}_1 \cap \mathcal{S}_2 = \emptyset$ , and  $\mathcal{S}_1 \cup \mathcal{S}_2 = \mathcal{C} \cup \mathcal{D}$ . We define two properties whose verification implies verification of the inevitability of the equilibrium.

#### Property 1.

$\forall x(0, j) \in \mathcal{S}_1, x(t, j) \rightarrow x_e$  for  $t \rightarrow \infty$ .

#### Property 2.

$\forall x(0, j) \in \mathcal{S}_2 = (\mathcal{C} \cup \mathcal{D}) \setminus \mathcal{S}_1, x(t, j) \in \mathcal{S}_1$  for  $t \rightarrow b \in \mathbb{R}_{>0}$ .

If we denote the inevitability property by  $\varphi$ , Property.1 by  $\varphi_1$  and Property.2 by  $\varphi_2$ , then  $\varphi = \varphi_1 \wedge \varphi_2$ . A hybrid arc  $x$  satisfies  $\varphi$  iff it satisfies  $\varphi_1$  in  $\mathcal{S}_1$  and  $\varphi_2$  in  $\mathcal{S}_2$  i.e.,  $\forall x \in \mathcal{C} \cup \mathcal{D}, x \models \varphi \iff (x \models \varphi_1 \forall x \in \mathcal{S}_1) \wedge (x \models \varphi_2 \forall x \in \mathcal{S}_2)$ . We verify property  $\varphi_1$  by the deductive Lyapunov theory, and use a combination of bounded advection and Escape certificate for the verification of property  $\varphi_2$ .

**Theorem 2.**

If there are feasible Lyapunov certificates (fulfilling Th. 1),  $\{V_1, V_2, V_3\}$ , then,  $x \models \varphi_1, \forall x(0, j) \in \mathcal{S}1 = \{(V_1 \leq \gamma_{1_{max}}) \cup (V_2 \leq \gamma_{2_{max}}) \cup (V_3 \leq \gamma_{3_{max}})\}$ , and  $\mathcal{AI} = \mathcal{S}1$  is the attractive invariant set.

*Proof.* Follows directly from Th. 1, since the level sets defined by the level curves of the Lyapunov certificates represent attractive invariant sets with the negative Lie-derivative along the system trajectories. Therefore, eventually all system trajectories starting in these level sets converge to the equilibrium point.  $\square$

Following Th. 2, we encode the verification of  $\varphi_1$  as two SOS programs. The truth value of  $\varphi_1$  depends on the existence of the attractive invariant set  $\mathcal{S}1$ . The set  $\mathcal{S}1$  is computed from the maximized level sets defined by the three candidate Lyapunov certificates  $V_1, V_2, V_3$ . We compute these certificates using a sound mathematical technique called the S-procedure [3]. The first SOS program is given below:

- (a)  $V_i(x) - \sum_{k=1}^{n_{C_i}} s_1^{(ik)}(x)g_{ik}(x) \in \mathcal{S}_n, \forall x \neq 0,$   
 $i \in \{1, 2, 3\}, \forall k \in \{1, \dots, n_{C_i}\}, s_1^{(ik)} \in \mathcal{S}_n,$
- (b)  $\left( -\frac{\partial V_i}{\partial x}(x)F_i(x, u) - \sum_{k=1}^{n_{C_i}} s_2^{(ik)}(x)g_{ik}(x) - \sum_{j=1}^m s_3^j(x)a_j(u) \right) \in \mathcal{S}_n,$   
 $\forall i \in \{1, 2, 3\}, \forall k \in \{1, \dots, n_{C_i}\}, \forall j \in \{1, \dots, m\},$   
 $(s_2^{(ik)}, s_3^j) \in \mathcal{S}_n,$
- (c)  $\left( V_j(x) - V_{j'}(G_i(x)) - s_4^{(i0)}(x)h_{i0}(x) - \sum_{k=1}^{m_{D_i}} s_5^{(ik)}(x)h_{ik}(x) \right) \in \mathcal{S}_n, \forall j \forall j' \in \{1, 2, 3\},$   
 $j \neq j', \forall i \in \{1, 2, 3, 4\}, \forall k \in \{1, \dots, n_{D_i}\},$   
 $s_4^{(i0)} \geq 0, s_5^{(ik)} \in \mathcal{S}_n$

Here  $V_i(x)$ ,  $V_j(x)$ ,  $V_{j'}(x)$ ,  $s_1^{(ik)}$ ,  $s_2^{(ik)}$ ,  $s_3^j$ ,  $s_4^{(i0)}$ ,  $s_5^{(ik)}$ , are polynomials of degree  $d$ .

SOS constraints (a) and (b) enforce positive definiteness on the Lyapunov certificates, and negative semi-definiteness on their Lie-derivatives respectively. Furthermore, these constraints have to be satisfied in their respective domains  $C_i$ 's, where,  $C_i = \{x \in \mathbb{R}^n : g_{ik} \geq 0, \text{ for } k \in \{1, \dots, n_{C_i}\}, i \in \{1, 2, 3\}\}$ . Constraint (b) also ensures parameters  $u$  to belong to the set,  $\{a(u) \geq 0, \text{ for } j \in \{1, \dots, m\}\}$ . Constraint in (c) ensures that Lyapunov certificates  $V_j(x)$  decrease along the discrete jumps in the sets,  $D_i = \{x \in \mathbb{R}^n : h_{ik} \geq 0, h_{i0} = 0, \text{ for } k \in \{1, \dots, n_{D_i}\}, i \in \{1, 2, 3, 4\}\}$ , through the mappings  $G_i(x)$ 's. SOS polynomials  $s_1^{(ik)}$ ,  $s_2^{(ik)}$ ,  $s_3^j$ ,  $s_4^{(i0)}$ ,  $s_5^{(ik)}$  are used to enforce domain constraints through the S-procedure. A feasible solution of the above SOS program results in Lyapunov certificates  $V_i$ . If this SOS program is infeasible, then either the program is repeated for an increased degree  $d$  of the polynomials, or we conclude that the truth value of the property  $\varphi_1$  can not be established.

The second SOS program for maximizing the level curves for every  $V_i \leq \gamma_i \in \mathbb{R}_{>0}$ , is,

$$\begin{aligned} & \text{maximize} && \gamma_i \\ & \text{subject to} && s5 + \sum_{k=1}^{n_{C_i}} s6^{ik}(-g_{ik}) - (V_i - \gamma_i) + \epsilon = 0, \\ & && (s5, s6^{ik}) \in \mathcal{S}_n, i \in \{1, 2, 3\}, k \in \{1, \dots, n_{C_i}\}. \end{aligned}$$

This algorithm maximizes the level curves of the Lyapunov certificates  $V_i$  such that  $\mathcal{Z}(V_i - \gamma_i) \subset \mathcal{Z}(-g_{ik})$ , for  $k \in \{1, \dots, n_{C_i}\}$  (Lemma. 1). The set  $\mathcal{S}1 = \bigcup_{i=1}^3 (V_i \leq (\gamma_i)_{max})$ . The non-emptiness of the set  $\mathcal{S}1$  shows that  $x \models \varphi_1, \forall x \in \mathcal{S}1$ .

To verify property  $\varphi_2$ , we extend the advection of level sets [15] to hybrid systems, and show that for all  $x(0, j) \in \mathcal{S}2, x(t, j) \rightarrow \mathcal{S}1$  for some bounded  $t$ . This is given in Alg. 1 where Line-5 is the set advection implemented as a SOS program. Let the sets,  $\mathcal{S}2 = \mathcal{Z}(P_{Initial})$ , and  $\mathcal{S}2_{next} = \mathcal{Z}(P_{next})$ , where,  $(P_{Initial}, P_{next}) : \mathbb{R}^n \rightarrow \mathbb{R}$  are polynomial functions. The SOS algorithm for bounded advection of level sets is given below,

$$\begin{aligned} & \text{minimize} && \eta \\ & \text{s.t.} && P_{next}(0) < 0, \\ & && \nabla P_{next} \cdot (v_1, v_2, \phi_{ref} - \phi_{VCO})^T > 0, \\ & && s1_i - s2_i P_{Initial} + A_{-h} P_{next} + \eta + \sum_{k=1}^{m_{C_i}} s3_{ik} g_{ik} = 0, \\ & && s4_i + s5_i (P_{Initial} - \mu) - A_{-h} P_{next} + \eta + \sum_{k=1}^{m_{C_i}} s6_{ik} g_{ik} = 0, \\ & && s7_i - s8_i (P_{Initial} - \mu) + \sum_{k=1}^{m_{C_i}} s9_{ik} g_{ik} + \nabla^2 P_{next} \frac{h^2}{2} - \eta = 0, \\ & && s10_i - s11_i (P_{Initial} - \mu) + \sum_{k=1}^{m_{C_i}} s12_{ik} g_{ik} \nabla^2 P_{next} \frac{h^2}{2} - \eta = 0, \\ & && (s1_i, s2_i, s3_{ik}, s4_i, s5_i, s6_{ik}, s7_i, s8_i, s9_{ik}, s10_i, s11_i, \\ & && s12_{ik}) \in \mathcal{S}_n. \end{aligned} \tag{6}$$

Here  $P_{next}$  is of degree  $d_r$ ,  $\mu > 0$ ,  $\eta > 0$ ,  $h > 0$ ,  $u \in [L U]$ , and  $s1_i, s2_i, s3_{ik}, s4_i, s5_i, s6_{ik}, s7_{ik}, s8_i, s9_{ik}, s10_i, s11_i, s12_{ik}$ , are polynomials of degree  $d$ .

The first two constraints of this SOS program ensure the advected level sets are closed and connected (See [15] and the references there in). The next two constraints search for a polynomial  $P_{next}$ , such that when the set  $\mathcal{Z}(P_{next})$  is backward advected by the first order Taylor advection map  $A_{-h}$ , we obtain a set such that,

$$\begin{aligned} \mathcal{Z}(P_{Initial}) &\subset \mathcal{Z}(A_{-h} P_{next} + \eta) \subset \psi_{-h}(\mathcal{Z}(P_{next})) \subset \\ \mathcal{Z}(A_{-h} P_{next} - \eta) &\subset \mathcal{Z}(P_{Initial} - \mu). \end{aligned}$$

Here  $\mu$  is used as a precision parameter determining, how closely we want the set  $\mathcal{Z}(P_{Initial})$  to be approximated by the set  $\mathcal{Z}(A_{-h} P_{next} + \|\eta\|)$ . The last two constraints enforce the truncation error of the first order Taylor approximation such that,  $\|\nabla^2 P_{next} \frac{h^2}{2}\| \leq \eta$ , in the set  $\mathcal{Z}(P_{Initial} - \mu)$ . Note that we solve the above optimization SOS program by using bisection on  $\eta$ . To be conservative, and use an over-approximation to the set  $\psi_h(\mathcal{Z}(P_{Initial}))$ , the set membership in Line-6 of the Alg. 1, is encoded as a SOS program utilizing Lemma. 1 for the sets  $\mathcal{Z}(P_{next} - \eta)$  and  $\mathcal{S}1$ .

---

**Algorithm 1** Verification of Property  $\varphi_2$ 

**INPUT:** : Hybrid System Model of CP PLL, Sets  $S_1$ ,  $S_2$   
**OUTPUT:** :  $\varphi_2$  Verified in Bounded Time/No-answer

- 1:  $S_{2\text{next}} \leftarrow \emptyset$
- 2:  $S_{2\text{advect}} \leftarrow \emptyset$
- 3:  $S_{2\text{advect}} \leftarrow S_2$
- 4: **for**  $j \leftarrow 1$  to  $j \leftarrow m$  **do**
- 5:    $S_{2\text{next}} \leftarrow \text{Advect}(S_{2\text{advect}})$
- 6:   **if**  $S_{2\text{next}} \not\subset S_1$  **then**
- 7:      $S_{2\text{advect}} \leftarrow S_{2\text{next}}$
- 8:   **else**
- 9:      $x \models \varphi_2, \forall x \in S_2$
- 10:    **break**
- 11:   **end if**
- 12: **end for**
- 13: Try a large value of  $m$
- 14: **if**  $S_{2\text{next}} \not\subset S_1$  **then**
- 15:   For  $S_{2\text{next}} \setminus (S_{2\text{next}}' = S_1 \cap S_{2\text{next}})$  find the Escape Certificate E.
- 16:   **if** E exists **then**
- 17:      $x \models \varphi_2, \forall x \in S_2$
- 18:     **break**
- 19:   **else**
- 20:     No Answer about  $\varphi_2$
- 21:   **end if**
- 22: **end if**

---

**Remark 2.**

As for the transformed CP PLL hybrid system (Remark.1) we have identity jump maps, there is therefore no need of constraints on the level sets due to discrete jumps.

After each iteration of the advection of level sets, if the set inclusion  $S_{2\text{next}} \subset S_1$  is true, then property  $\varphi_2$  is verified. Alternatively, the algorithm keeps on advecting the set  $S_{2\text{next}}$  for a user defined bounded number of iterations (Line 7-13). If the property  $\varphi_2$  is still not verified (This can happen when the advection of the level sets is unsymmetrical and a subset of the set  $S_2$  is not immersed in  $S_1$ ), we compute the Escape certificate E for the set,  $S_{2\text{next}} \setminus S_{2\text{next}}'$ , showing that trajectories in this set will eventually leave and reach  $S_1$  (Line 14-18, as they can not reach  $S_{2\text{next}}'$ ). This either results in the verification of the property  $\varphi_2$  (respectively  $\varphi$ ) in the set  $S_2$  or we conclude inconclusiveness about the truth value of  $\varphi_2$  (respectively  $\varphi$ ). Line 15 is implemented by the following SOS program,

$$-\frac{\partial E_i}{\partial x}(x)F_i(x, u) - s_1(x)g_2(x) + s_2(x)g_2'(x) + \varepsilon \in \mathcal{S}_n$$

$$(s_1, s_2) \in \mathcal{S}_n$$

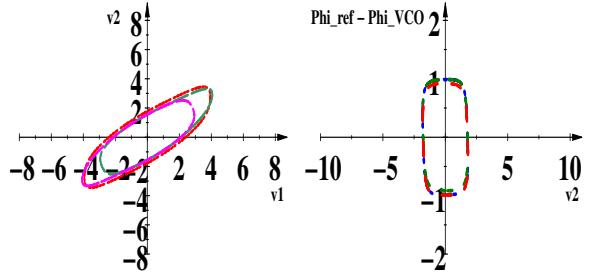
where,  $\mathcal{S}_{2\text{next}} := g_2(x) \geq 0$ , and  $\mathcal{S}_{2\text{next}}' := g_2'(x) \geq 0$ .

## 4. EXPERIMENTAL EVALUATION

We used YALMIP [7] solvers within MATLAB for the verification of the inevitability property (respectively sub-properties) on a 2.6 GHZ Intel Core i5 machine with 4 GB of memory. The CP PLL parameters are listed in Table. 1, with all phases normalized by  $2\pi$ . We computed degree-6 multiple Lyapunov certificates for the third order, and degree-4 multiple Lyapunov certificates for the fourth order CP PLL. Their attractive invariant sets as projected onto different planes are shown in Fig. 2, and Fig. 3. Results of their bounded

Parameters	Third Order	Fourth Order
$C_1$	$[1.98 \ 2.2]e - 12F$	$[31 \ 29]e - 12F$
$C_2$	$[6.1 \ 6.4]e - 12F$	$[3.2 \ 3.4]e - 12F$
$C_3$		$[1.8 \ 2.2]e - 12F$
$R$	$[7.8 \ 8.2]e3\Omega$	$[48 \ 52]e3\Omega$
$R2$		$[7 \ 9]e3\Omega$
$f_{ref}$	27MHZ	5MHZ
$f_O$	$27e3\text{MHZ}$	5MHZ
$I_p$	$[495 \ 505]e-6A$	$[395 \ 405]e-6A$
$K_p$	$[198 \ 202]$	$[495 \ 502]$

**Table 1:** PLL Parameters used in the Experimentation



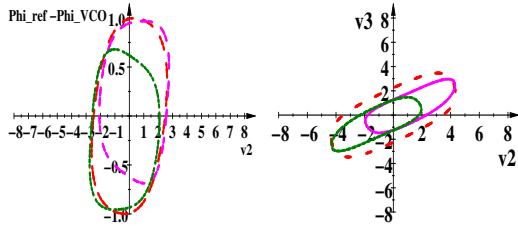
**Figure 2:** 3-Order AIL Projected onto  $(v_1, v_2)$ , and  $(v_2, (\phi_{ref} - \phi_{VCO})$

advection are shown in Fig. 4 and Fig. 5 respectively. Note that due to space constraint, we have shown projections on only two planes for each benchmark. The outer set plotted in solid is the initial set inside which we aim to prove the inevitability of the phase-locking in the CP PLL. The advected level curves are shown in dotted. We used the time step  $h=1e-3$  seconds, and  $\mu = 1e-4$  in the computation of advected sets. It can be observed, for the third order level sets eventually symmetrically immersed in the central attractive set after bounded iterations. For the fourth order CP PLL, the advection of level sets is unsymmetrical as the progress in one direction is more abrupt than another. We have therefore the level sets immersed in the attractive invariant from one direction, but advection is inconclusive for a subset in the other direction shown by the pink shaded area in Fig. 5. For the inconclusive subset, we searched a degree-4 Escape certificate for two modes (In one mode2 bounded advection proved convergence to attractive invariant set) showing convergence of the trajectories to the attractive invariant. Computation time of different steps of our verification methodology are given in Table. 2.

Results show the effectiveness of our approach to the verification of the inevitability property of a complex real circuit. We have proved the inevitability property avoiding hundred of discrete transitions as well as the complex continuation as in [2]. Computation time is comparable to [2], and infact is less by an order of atleast considering their approach using gridding of the state space for a third order PLL only. Though user input is needed in the formalization of the problem, our Lyapunov and Escape certificate based deductive methods are applicable to infinite domain (oppose to bounded) and avoid approximating (under or over) solutions of the differential equations. Furthermore, our bounded advection of level sets has the advantage of dealing with larger sets in a single iteration as compare to the existing bounded model checking approaches.

Verification Step	3-Order Time(Sec)	4-Order Time(Sec)
Attractive Invariant	1381.7(Degree 6)	10021(Degree 4)
Max.Level Curves	15.5	12
Advection	106.8487 (14 iterations)	140.678 (7 Iterations)
Checking Set Inclusion	13	10.2
Escape Certificate		18 (2 Certificates)

**Table 2:** Computation Time of the Inevitability Verification



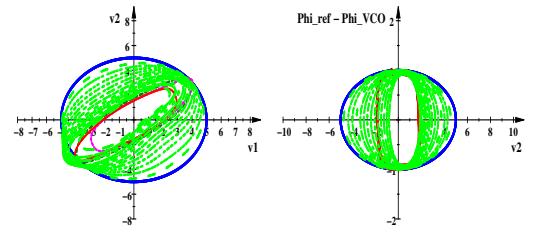
**Figure 3:** 4-Order  $\mathcal{A}\mathcal{I}$  Projected onto  $(v_2, v_3)$ , and  $(v_2, (\phi_{ref} - \phi_{VCO}))$

## 5. CONCLUSION

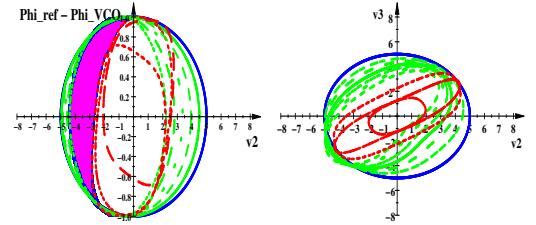
We have presented a scalable verification methodology benefiting from both deductive and bounded verification approaches. We tailored these approaches to verify the complex inevitability property for a practical AMS CP PLL circuit of higher order. As the problem is known to be NP-hard, we used the sufficient SOS relaxation algorithm for the verification of these properties. Experimental results show the effectiveness of our methodology avoiding expensive discretization and reach set computations.

## 6. REFERENCES

- [1] T. A. Henzinger, P.-H. Ho, and H. Wong-Toi. Algorithmic analysis of nonlinear hybrid systems. *IEEE Transactions on Automatic Control*, 43(4):540–554, Apr 1998.
- [2] M. Althoff, A. Rajhans, B. H. Krogh, S. Yaldiz, X. Li, and L. Pileggi. Formal verification of phase-locked loops using reachability analysis and continuization. In *Proceedings of the International Conference on Computer-Aided Design ICCAD*, pages 659–666, 2011.
- [3] S. P. Boyd, L. El Ghaoui, E. Feron, and V. Balakrishnan. *Linear matrix inequalities in system and control theory*, volume 15. SIAM, 1994.
- [4] R. Goebel, R. G. Sanfelice, and A. R. Teel. *Hybrid dynamical systems: modeling, stability, and robustness*. Princeton University Press, 2012.
- [5] H. K. Khalil. *Nonlinear Systems*. Prentice Hall, third edition, 2002.
- [6] H. Lin, P. Li, and C. J. Myers. Verification of digitally-intensive analog circuits via kernel ridge regression and hybrid reachability analysis. In *Proceedings of the 50th Annual Design Automation Conference*, page 66. ACM, 2013.
- [7] J. Lofberg. Yalmip: A toolbox for modeling and optimization in matlab. In *Computer Aided Control Systems Design, 2004 IEEE International Symposium on*, pages 284–289. IEEE, 2004.
- [8] J. Lygeros, K. H. Johansson, S. N. Simić, J. Zhang, and S. S. Sastry. Dynamical properties of hybrid automata. *IEEE TRANSACTIONS ON AUTOMATIC CONTROL*, 48(1):2–17, January 2003.
- [9] A. Papachristodoulou and S. Prajna. On the construction of lyapunov functions using the sum of squares decomposition. In *41st IEEE Conference on Decision and Control*, volume 3, pages 348–3487, 2002.
- [10] P. A. Parrilo. *Structured semidefinite programs and semialgebraic geometry methods in robustness and optimization*. PhD thesis, Citeseer, 2000.
- [11] S. Prajna and A. Jadbabaie. Safety verification of hybrid systems using barrier certificates. In *Hybrid Systems: Computation and Control*, pages 477–492. Springer, 2004.
- [12] S. Prajna and A. Papachristodoulou. Analysis of switched and hybrid systems-beyond piecewise quadratic methods. In *American Control Conference, 2003. Proceedings of the 2003*, volume 4, pages 2779–2784. IEEE, 2003.
- [13] T. Sturm and A. Tiwari. Verification and synthesis using real quantifier elimination. In *Proceedings of the 36th international symposium on Symbolic and algebraic computation*, pages 329–336. ACM, 2011.
- [14] A. Taly and A. Tiwari. Deductive verification of continuous dynamical systems. In *LIPICS-Leibniz International Proceedings in Informatics*, volume 4. Schloss Dagstuhl-Leibniz-Zentrum für Informatik, 2009.
- [15] T. Wang, S. Lall, and M. West. Polynomial level-set method for polynomial system reachable set estimation. 2013.
- [16] J. Wei, Y. Peng, G. Yu, and M. Greenstreet. Verifying global convergence for a digital phase-locked loop. In *Formal Methods in Computer-Aided Design (FMCAD), 2013*, pages 113–120. IEEE, 2013.
- [17] M. H. Zaki, S. Tahar, and G. Bois. Formal verification of analog and mixed signal designs: A survey. *Microelectronics Journal*, 39(12):1395–1404, 2008.



**Figure 4:** 3-Order Advection Projected onto  $(v_1, v_2)$ , and  $(v_2, (\phi_{ref} - \phi_{VCO}))$



**Figure 5:** 4-Order Advection Projected onto  $(v_2, v_3)$ , and  $(v_2, (\phi_{ref} - \phi_{VCO}))$